

Appl. No. TBD
Preliminary Amdt. Dated March 29, 2005
Reply to Office action of N/A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : TBD
Applicant : Hiroyuki Yoshikawa et al.
Filed : Herewith
Title : BROADBAND MODULATION PLL, TIMING ERROR
CORRECTING SYSTEM FOR BROADBAND MODULATION PLL,
MODULATION TIMING ERROR CORRECTING METHOD AND
METHOD OF ADJUSTING RADIO COMMUNICATION DEVICE
HAVING BROADBAND MODULATION PLL

Int'l. Application No. : PCT/JP04/011506
Int'l. Filing Date : August 4, 2004

Customer No. : 000,116
Docket No. : 38038

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT "A"

Sir:

Please amend the above-identified application, prior to examination thereof, in the following manner.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.

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